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Application Number	10/606,753	
Filing Date	June 27, 2003	
First Named Inventor	Robert B. MAGILL et al.	
Group Art Unit	2661	
Examiner Name	Unassigned	
Attorney Docket Number	32172-188433	

		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS	
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T 2
/BTO/	Al	CHUANG et al., "Matching Output Queueing With A Combined Input/Output-Queued Switch," IEEE Journal on Selected Areas In Communications, Vol. 17, No. 6, pp. 1030-1039, (June, 1999).	
/BTO/	A2	STOICA et al., "Exact Emulation of an Output Queueing Switch By a Combined Input Output Queueing Switch," presented at 6th IEEE/IFIP IWQoS, pp. 218-224, (1998).	
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Examiner /Brian O'Connor/ (03/13/2007)	Date Considered	03/13/2007
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/BTO/	Al	DONF Scala	DONPAUL C. STEPHENS et al., "Implementing Distributed Packet Fair Queueing in a Scalable Switch Architecture", Proc. IEEE INFOCOM 1998, pp. 282-290, April 1998.							
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